

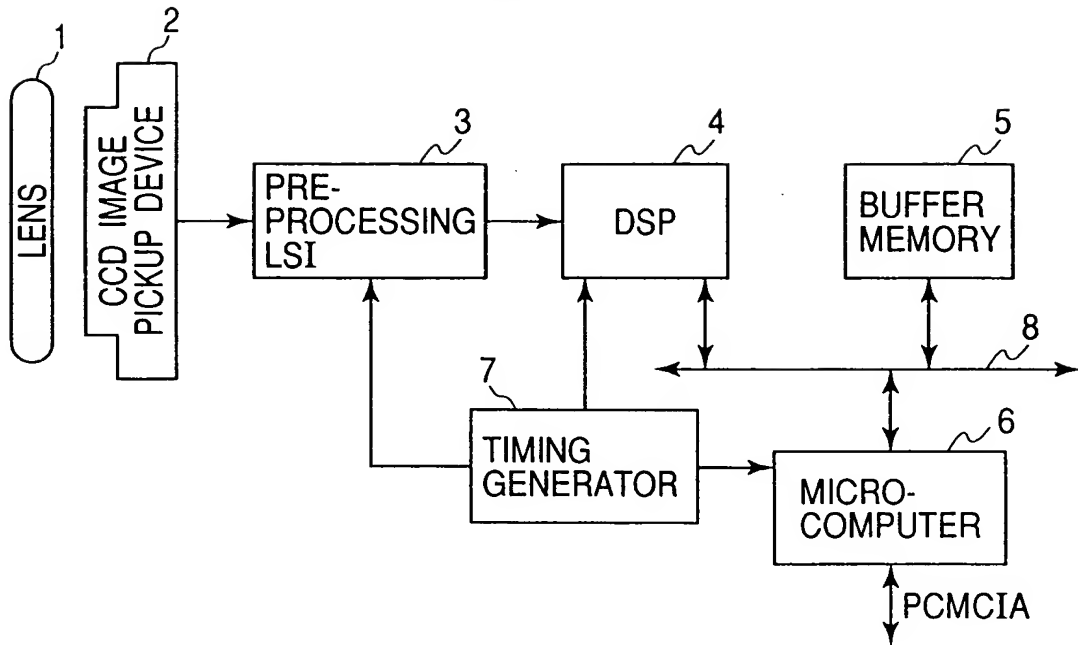
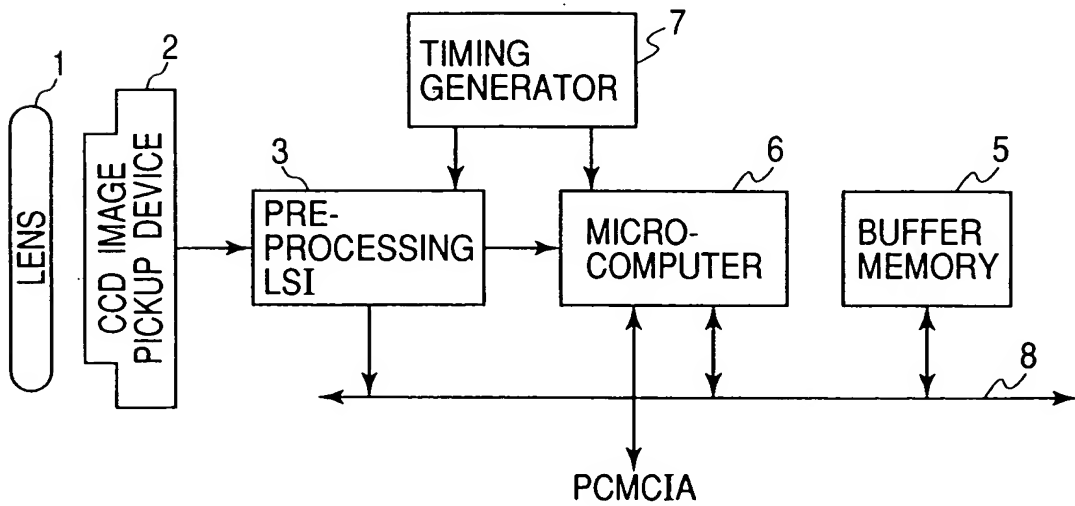
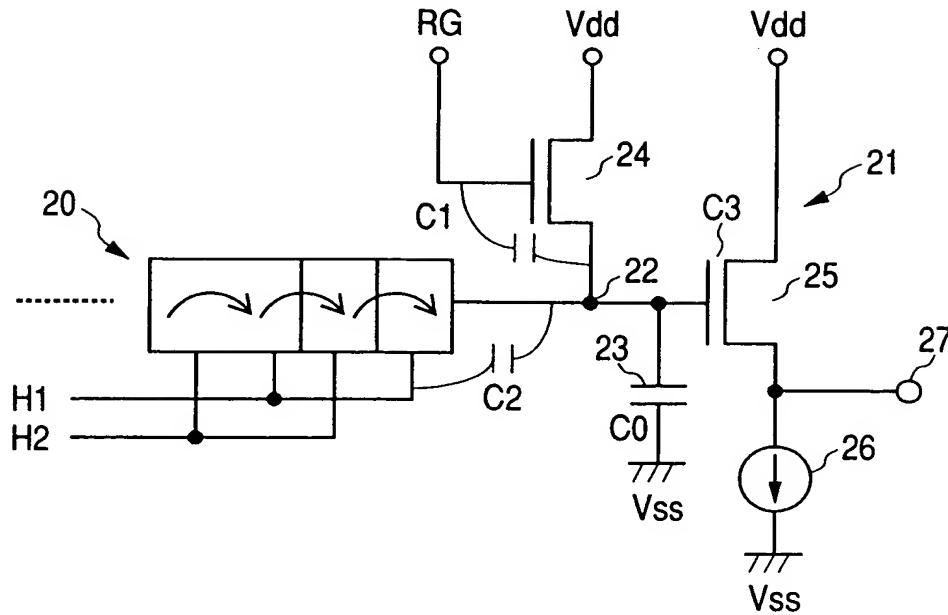
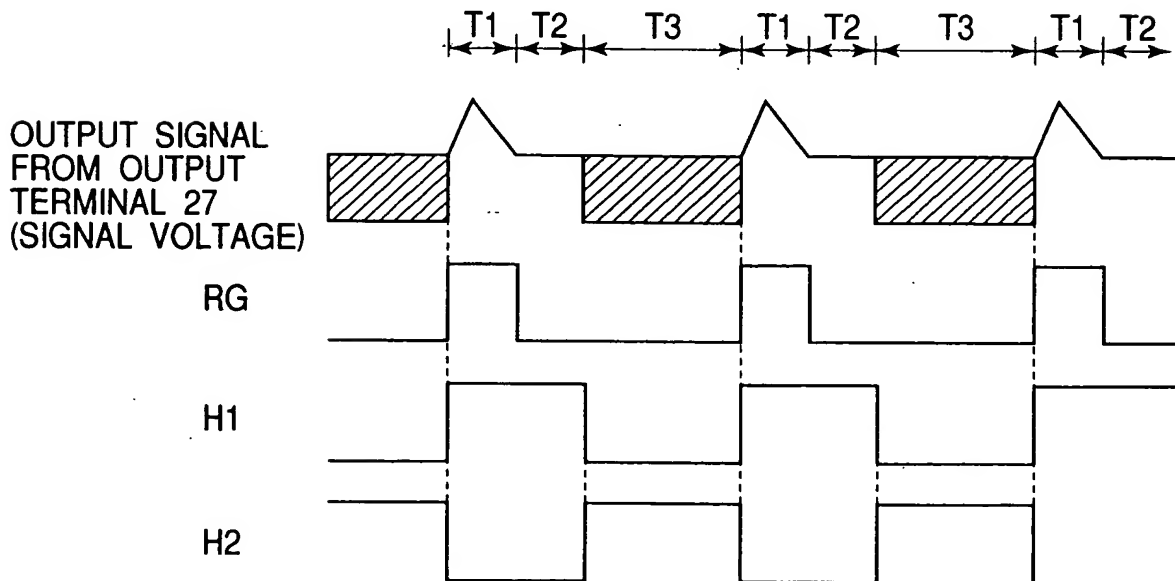
FIG. 1**FIG. 2**

FIG. 3**FIG. 4**

OUTPUT SIGNAL FROM OUTPUT
 TERMINAL 27
 (SIGNAL VOLTAGE)

RG

H1

H2

OUTPUT TIMING OF SOURCE FOLLOWER
 OUTPUT CIRCUIT

FIG. 5

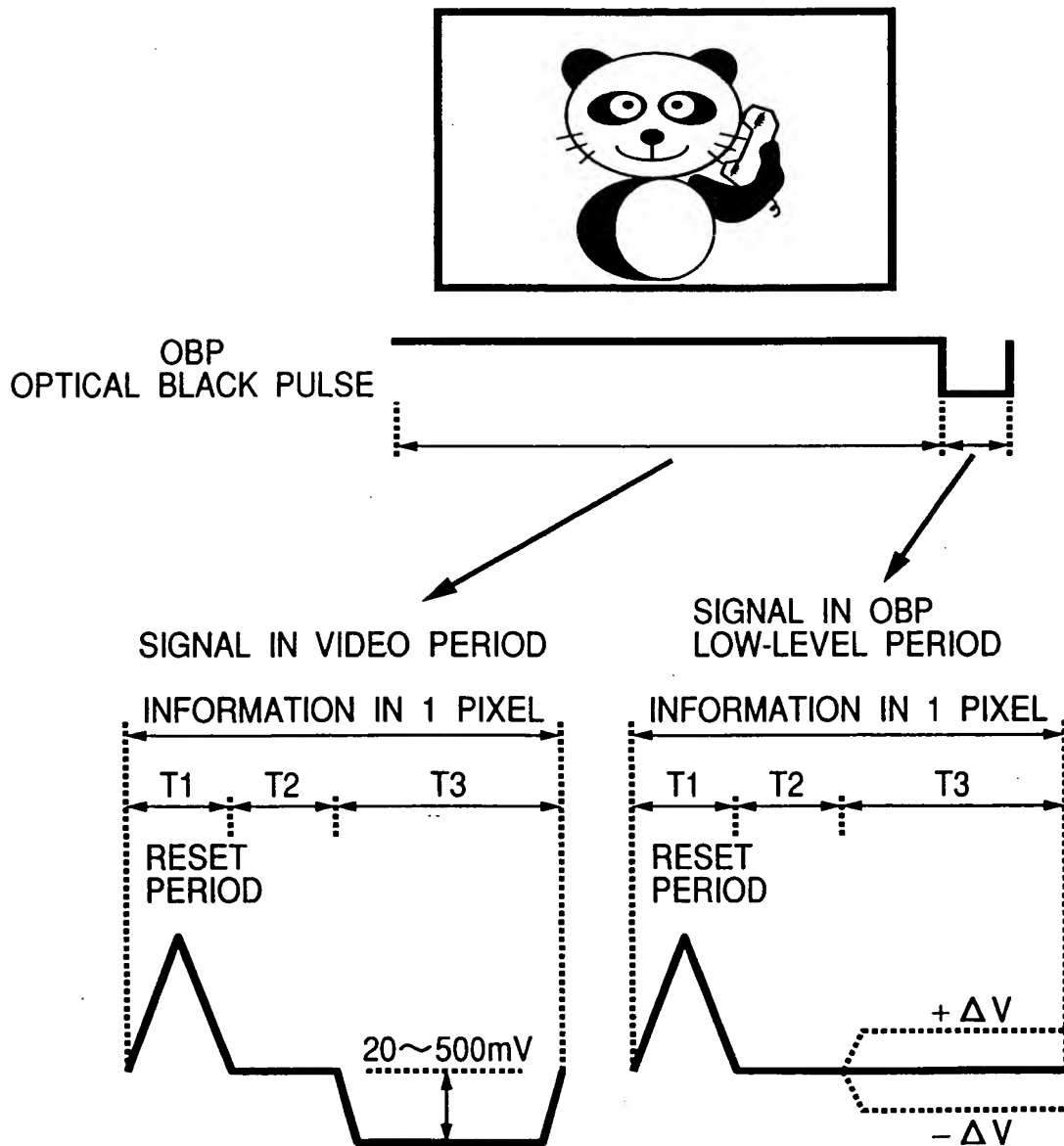


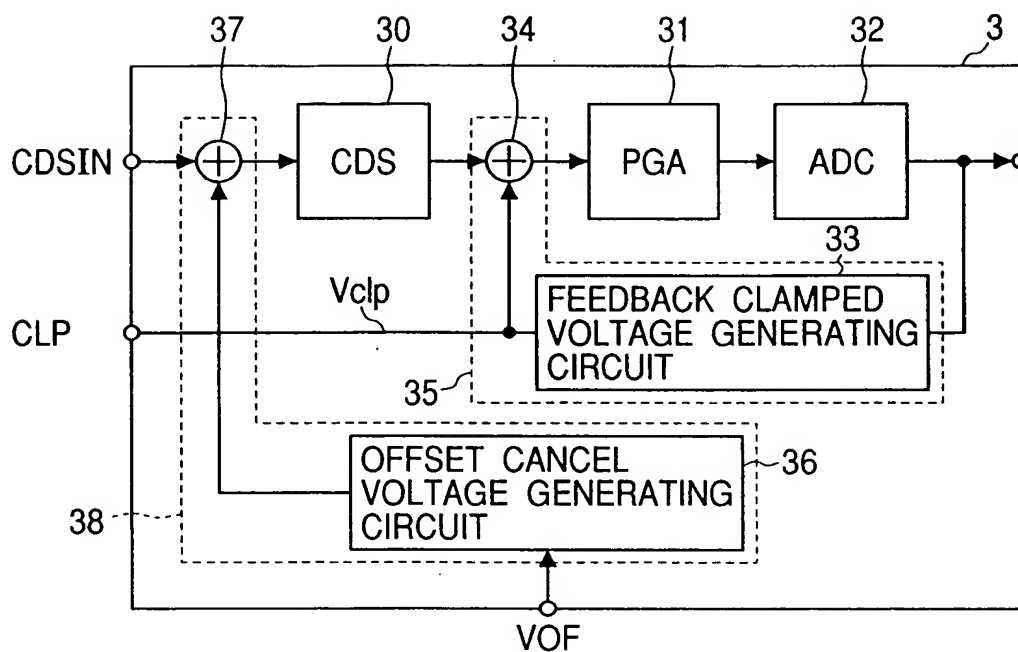
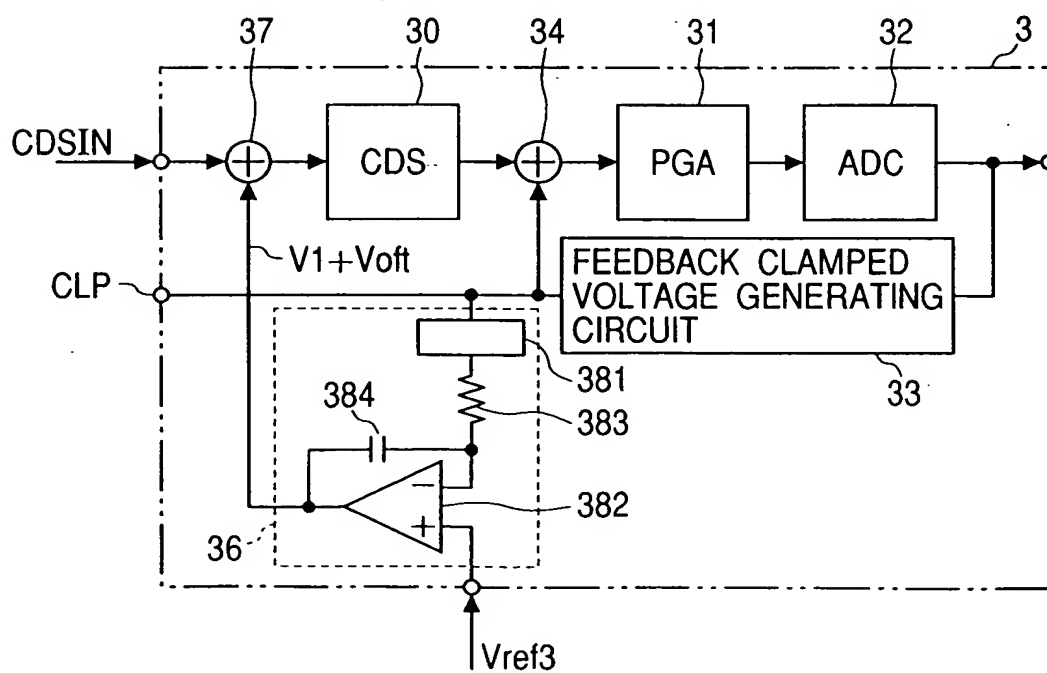
FIG. 6**FIG. 11**

FIG. 7

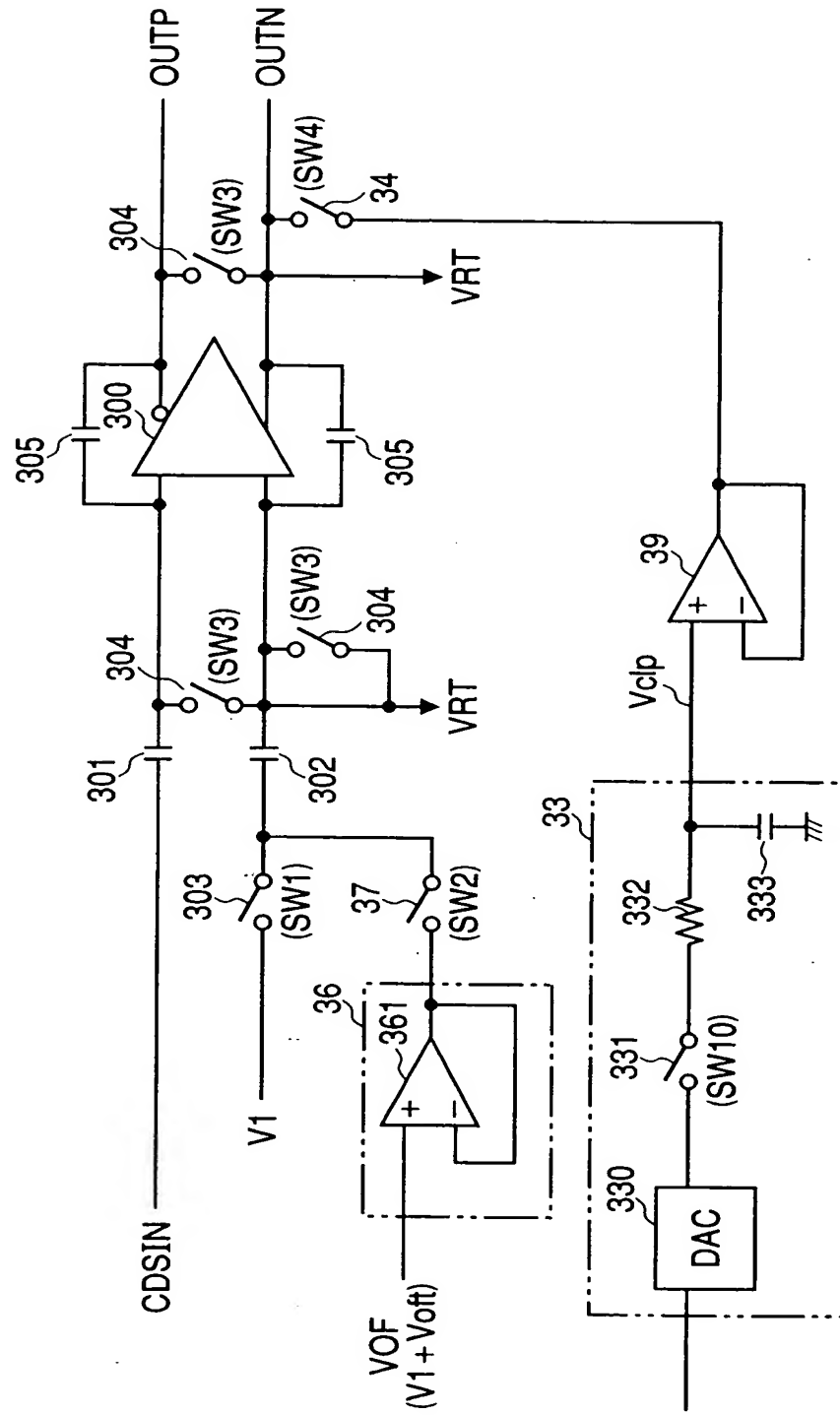


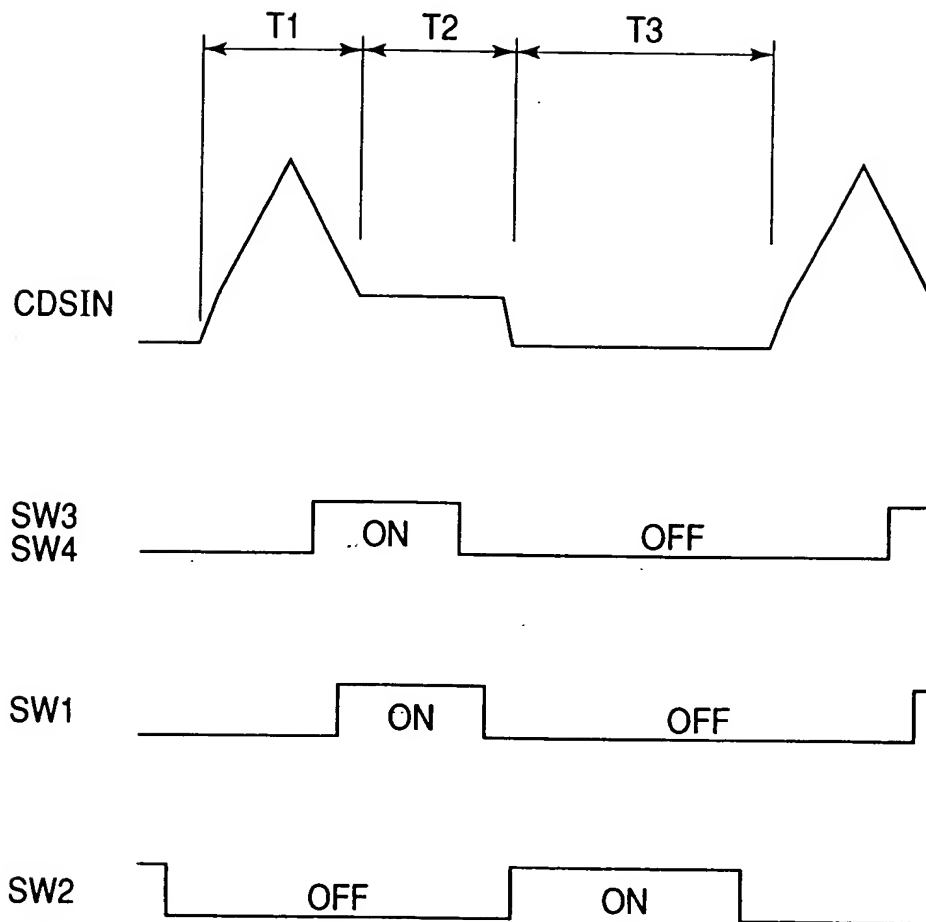
FIG. 8

FIG. 9

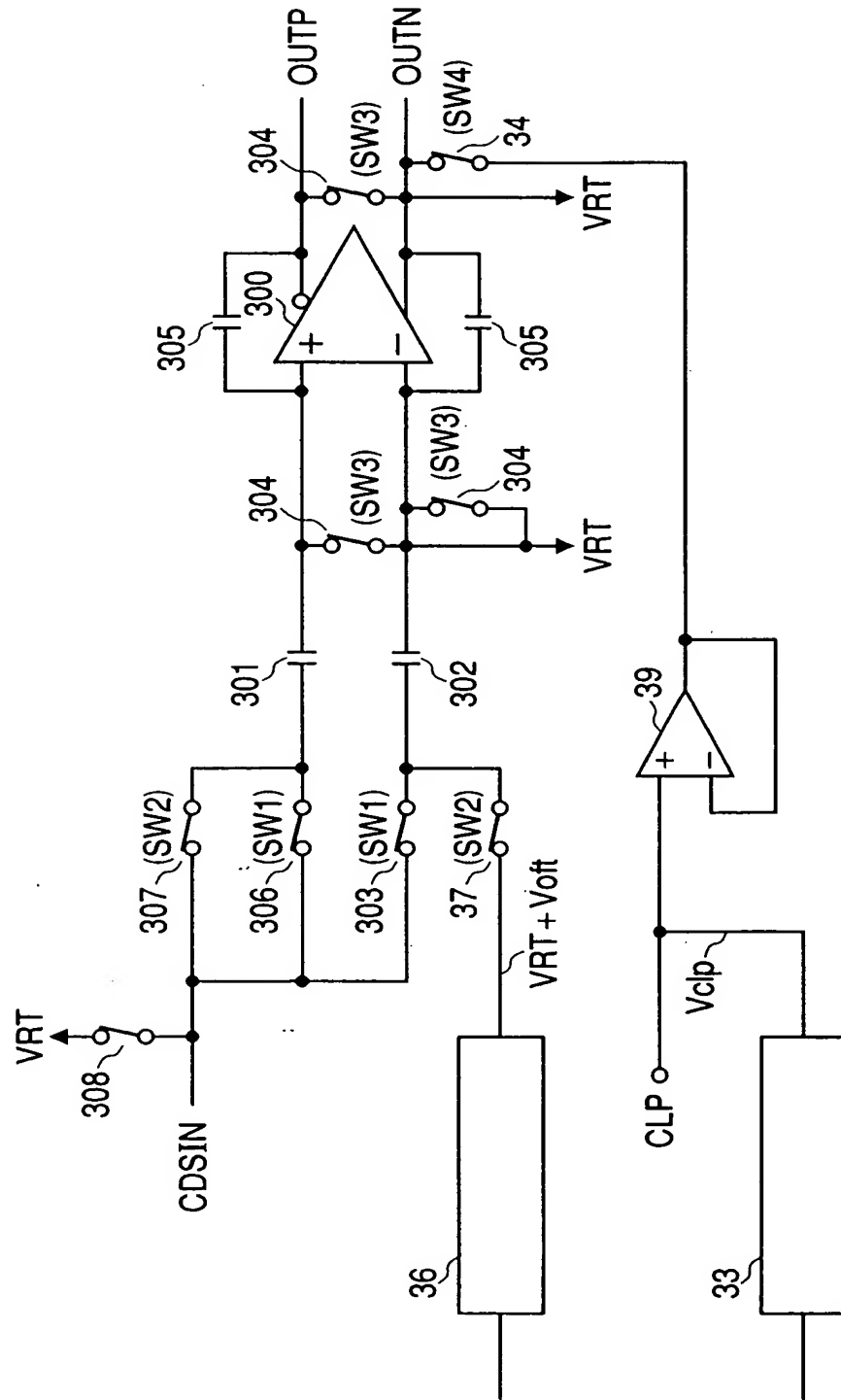


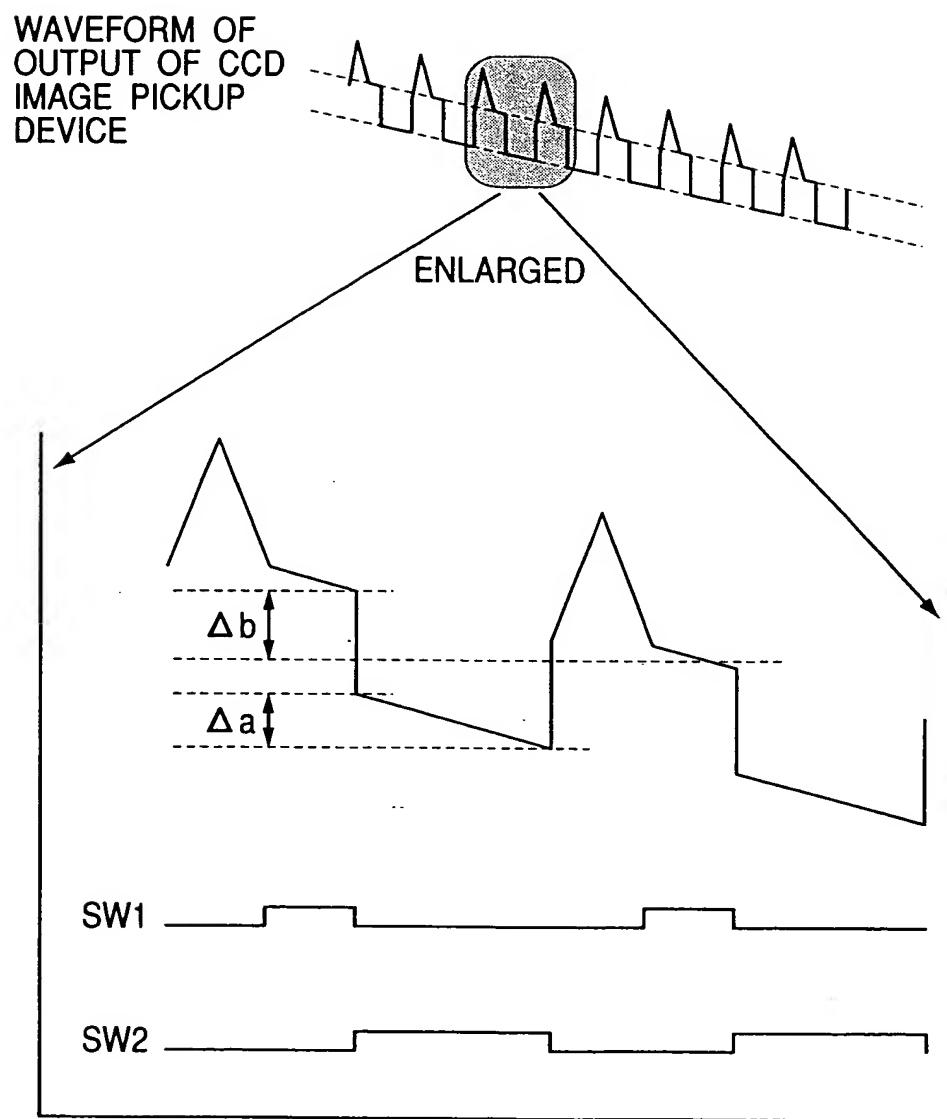
FIG. 10

FIG. 12

